

PTO-1449 REPRODUCED  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  September 26, 2003  (Use several sheets if necessary)	ATTORNEY DOCKET NO. 2390.1001-012	CONT. OF APPLICATION NO. 09/887,960	
	APPLICANT William J. Dally, <i>et al.</i>		
	FILING DATE	CONFIRMATION NO.	GROUP 2609

U.S. PATENT DOCUMENTS				
EXAM- INER INI- TIAL	REF. NO.	DOCUMENT NUMBER	ISSUE DATE / PUBLICATION DATE	NAME
/A.C.	AA	4,118,094	10/03/78	Key
/A.C.	AB	4,340,092	07/20/82	Chisholm
/A.C.	AC	4,371,013	02/01/83	Camp
/A.C.	AD	4,469,388	09/04/84	Narozny
/A.C.	AE	4,495,615	01/22/85	Wilcke
/A.C.	AF	4,631,637	12/23/86	Romania et al.
/A.C.	AG	4,861,274	08/29/89	Brune et al.
/A.C.	AH	4,862,451	08/29/89	Closs et al.
/A.C.	AI	4,911,645	03/27/90	August et al.
/A.C.	AJ	4,933,933	06/12/90	Dally et al.
/A.C.	AK	5,088,091	02/11/92	Schroeder et al.
/A.C.	AA2	5,123,848	06/23/92	August et al.
/A.C.	AB2	5,134,690	07/28/92	Samatham
/A.C.	AC2	5,144,691	09/01/92	August et al.
/A.C.	AD2	5,155,784	10/13/92	Knott
/A.C.	AE2	5,172,371	12/15/92	Eng et al.
/A.C.	AF2	5,179,558	01/12/93	Thacker et al.
/A.C.	AG2	5,261,827	11/16/93	Lenzi et al.
/A.C.	AH2	5,355,372	10/11/94	Sengupta et al.
/A.C.	AI2	5,408,469	04/18/95	Opher et al.
/A.C.	AJ2	5,444,701	08/22/95	Cypher et al.
/A.C.	AK2	5,425,169	06/20/95	Steinman et al.
/A.C.	AA3	5,513,172	04/30/96	Shikama et al.
/A.C.	AB3	5,521,591	05/28/96	Arora et al.
/A.C.	AC3	5,532,856	07/02/96	Li et al.
/A.C.	AD3	5,581,705	12/03/96	Passint et al.

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FOREIGN PATENT DOCUMENTS						
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO	
/A.C./	AL	0 886 454 A2	12-23-98	Europe		
	AM					
	AN					
	AO					
	AP					
	AQ					
	AL2					
	AM2					
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	AP2					
	AQ2					
	AL3					
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	AQ3					
	AL4					
	AM4					
	AN4					
	AO4					
	AP4					
	AQ4					
	AL5					
	AM5					

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/A.C./	AR	Glass, C.J., and Ni, L.M., "The Turn Model for Adaptive Routing," Proc. 19th Internat. Symposium on Computer Architecture, 278-287 (May 1992).
/A.C./	AS	Rettberg, R., et al., "Development of a Voice Funnel System: Design Report," Bolt Beranek and Newman, Inc., Report No. 4098, 149 pages (August 1979).
/A.C./	AT	Dally, William, "Network and Processor Architecture for Message-Driven Computers." In VLSI and Parallel Computation, Suaya, R., and Birtwistle, G., eds. (San Mateo, CA:Morgan Kaufman), pp. 140-222 (1990).
/A.C./	AU	Dally, William J., "Virtual-Channel Flow Control," IEEE Transactions on Parallel and Distributed Systems, 3(2):194-205 (March 1992).
/A.C./	AV	Dally, W.J., and Seitz, C.L., "The torus routing chip," Distributed Computing, 1:187-196 (1986).
/A.C./	AW	Jesshope, Chris, "The MP1 Chip and its Application," Dept. of Electronic and Electrical Engineering, Univ. of Surrey, Guildford, Surrey GU2 5XH, UK, 47-54.
/A.C./	AX	Kessler, R.E., and Schwarzmeier, J.L., "CRAY T3D: A New Dimension for Cray Research," IEEE, 1063-6390/93, 176-182 (1993).
/A.C./	AY	Dally, William <i>et al.</i> , "Deadlock-Free Message Routing in Multiprocessor Interconnection Networks," IEEE Transactions on Computers, C-36(5):547-553 (May 1987).
/A.C./	AZ	Jesshope, C., and Izu, C., "The MP1 Network Chip," IEEE, 1066-6192/92, 338-348 (1992).
/A.C./	AR2	Dally, William J., et al., "The Reliable Router: A Reliable and High-Performance Communication Substrate for Parallel Computers," Proc. of Parallel Computer Routing and Communication Workshop, Seattle, WA, 241-255 (May 1994).
/A.C./	AS2	Dally, W.J., et al., "Architecture and Implementation of the Reliable Router," Proc. of Hot Interconnects II, Stanford, CA, 122-133 (August 11-13, 1994).
/A.C./	AT2	Nuth, Peter et al., "The J-Machine Network," Proc. of the Internat. Conference on Computer Design: VLSI in Computers and Processors, Cambridge, MA, 420-423 (October 1992).
/A.C./	AU2	Dennison, Larry R., "The Reliable Router: An Architecture for Fault Tolerant Interconnect," doctoral dissertation, Massachusetts Institute of Technology, Cambridge, MA, 444 pages (June 1996).

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